Data Structure I: Tutorial 5

Example of sizes and speeds of different types of memory:

	size	speed
L1 cache	32 KB	223 GB/s
L2 cache	256 KB	96 GB/s
L3 cache	8 MB	62 GB/s
RAM	$32~\mathrm{GB}$	23 GB/s
SDD	112 GB	448 MB/s
HDD	2 TB	112 MB/s
Internet	∞	$10 \mathrm{MB/s}$

Consider the following trivial program:

Initialize an array A of 32-bit integers of length n1 for (i=0; i+d < n; i+=d) do

2 | A[i] = i+d # Create a loop using every d-th position

3 A[i]=0 # Close the loop

- 4 for $(j=0; j<2^{28}; j++)$ do
- 5 | i=A[i] # Repeatedly walk on the loop

The following figure shows the running time depending on n and d.



Exercise 1 Estimate the number of cache misses for the following trivial algorithm transposing a matrix.

1 for $i \leftarrow 2$ to k do 2 for $j \leftarrow i + 1$ to k do 3 Swap(A_{ij}, A_{ji})

Exercise 2 The following algorithm splits a matrix of size $k \times k$ into submatrices of size $z \times z$ assuming z divides n. Estimate the number of cache misses.

```
# We split the matrix A into submatrices of size z \times z

1 for (i = 0; i < k; i + = z) do

2 for (j = i; j < k; j + = z) do

3 # We transpose the submatrix starting on position (i, j)

6 for (ii = i; ii < min(k, i + z); ii + +) do

5 for (jj = max(j, ii + 1); jj < min(k, j + z); jj + +) do

5 Swap(A_{ii,jj}, A_{jj,ii})
```

Exercise 3 In the assignment, implement the following recursive algorithm for matrix transposition.

```
1 Procedure transpose_on_diagonal(A)
      if matrix A is small then
\mathbf{2}
3
          Transpose matrix A using the trivial approach
      else
4
          A_{11}, A_{12}, A_{21}, A_{22} \leftarrow coordinates of submatrices
\mathbf{5}
          transpose_on_diagonal(A_{11})
6
          transpose_on_diagonal(A_{22})
7
          transpose_and_swap(A_{12}, A_{21})
8
9 Procedure transpose_and_swap(A, B)
```

```
if matrices A and B are small then
10
           Swap and transpose matrices A and B using the trivial approach
11
       else
12
           A_{11}, A_{12}, A_{21}, A_{22}, B_{11}, B_{12}, B_{21}, B_{22} \leftarrow coordinates of submatrices
13
           transpose_and_swap(A_{11}, B_{11})
14
           transpose_and_swap(A_{12}, B_{21})
15
           transpose_and_swap(A_{21}, B_{12})
16
           transpose_and_swap(A_{22}, B_{22})
\mathbf{17}
```

Exercise 4 In the recursive algorithm, describe splitting of a matrix which size is not a power of two.

Exercise 5 Why the tall-cache assumption is needed in the proof of I/O complexity on matrix transposition? What happens when this assumption is not satisfied?

The following compares the running time of building a binary heap depending on its size. The blue line shows the running time when all data are stored in heap's array. For the red line, the heap's array contains pointers to data and memory is allocated for every element separately.

